

What is claimed is:

1. An interface circuit conforming to multiple bus standards, comprising:
a first interface circuit conforming to a first bus standard;
a second interface circuit conforming to a second bus standard; and
5 a common set of pins coupled to the first interface circuit and the second interface circuit, the common set of pins being user selectable to communicate with a host computer bus in accordance with either the first bus standard or the second bus standard.
2. The interface circuit of claim 1, wherein the common set of pins transmit and
10 receive data conforming to either the PCI bus standard or the PCMCIA bus standard.
3. The interface circuit of claim 1, wherein the first bus standard is a PCI standard.
4. The interface circuit of claim 1, wherein the second bus standard is a
15 PCMCIA standard.
5. The interface circuit of claim 1, further comprising a multi-voltage input output buffer coupled to each pin.
6. The interface circuit of claim 1, further comprising an internal bus coupled to the first and second interface circuit.
- 20 7. The interface circuit of claim 6, wherein the first interface circuit formats signals on the internal bus to signals compliant with the first bus standard.
8. The interface circuit of claim 1, wherein the second interface circuit formats signals on the internal bus to signals compliant with the second bus standard.

9. The interface circuit of claim 1, further comprising a first power supply to supply voltage swings in accordance with the first bus standard.
10. The interface circuit of claim 1, further comprising a second power supply to supply voltage swings in accordance with the second bus standard.